



Attorney Docket No. 300.1160

CofC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Haruo SORIMACHI

Application No.: 10/828,328

ATTENTION

Confirmation No.: 4234

CERTIFICATE OF CORRECTION

Filed: April 21, 2004

BRANCH

U.S. Patent No.: 6,951,811

Batch No.:

Issued: October 4, 2005

For: A METHOD OF PRODUCING VIAS AND OTHER CONDUCTOR PARTS ON AN ELECTRODE TERMINAL FORMING SURFACE OF A SEMICONDUCTOR WAFER (As Amended)

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

*Certificate
JUL 10 2006
of Correction*

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: July 5, 2006

By: H. J. Staas
H. J. Staas
Registration No. 22,010

1201 New York Ave, N.W., 7th Floor
Washington, D.C. 20005
Telephone: (202) 434-1500
Facsimile: (202) 434-1501

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 6,951,811

DATED : October 4, 2005

INVENTOR(S) : Haruo SORIMACHI

In the specification, Column 4, line 52, after "board.", delete the paragraph return.

MAILING ADDRESS OF SENDER:
STAAS & HALSEY LLP
1201 New York Ave., N.W.
7th Floor
Washington, DC 20005

PATENT NO. 6,951,811

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